

CLAIMS:

1. A method of processing data corresponding to pixels of a sequence of digital images so as to detect blocking artefacts, said method comprising a step of high-pass filtering (110) a portion of a digital image, intended to supply at least one card of discontinuity pixels, and a step of detecting (120) blocking artefacts, intended to detect a first type (p1) of elementary blocking artefact from the at least one card of discontinuity pixels, said method
5 being characterized in that the detection step is also intended to detect a second type (p2) of elementary blocking artefact from the at least one card of discontinuity pixels.
2. A data processing method as claimed in claim 1, wherein the high-pass
10 filtering step (110) is suitable for using a wavelet transform, intended to supply at least one card of sub-sampled discontinuity pixels corresponding to a sub-band of said transform.
3. A data processing method as claimed in claim 2, also comprising a step of
15 localizing the elementary blocking artefact in a row of the portion of the digital image from signs of filtered values of the sub-sampled pixels of the card of discontinuity pixels.
4. A data processing method as claimed in claim 1, wherein the high-pass
filtering step is intended to supply two cards of discontinuity pixels, one horizontal card and one vertical card.
20
5. A data processing method as claimed in claim 1, wherein the high-pass
filtering step is suitable for using a gradient filter.
6. A data processing method as claimed in claim 1, comprising a step of
25 measuring the image quality, intended to provide a blocking artefact level from filtered values of the pixels corresponding to the blocking artefacts.
7. A data processing method as claimed in claim 1, comprising a step of
correcting the blocking artefacts in accordance with their type (p1, p2).

8. A television receiver comprising a processing device using the data processing method as claimed in claim 7, suitable for detecting blocking artefacts within a sequence of digital images and for correcting them with a view to displaying a sequence of corrected digital images on a screen of said receiver.
9. A video decoder suitable for providing a sequence of decoded digital images and comprising a processing device using the processing method as claimed in claim 7, suitable for detecting blocking artefacts within the sequence of decoded digital images and for correcting them so as to provide a sequence of corrected digital images.
10. A computer program product comprising a set of instructions which, when loaded into a circuit, cause said circuit to perform the method of processing digital images as claimed in any one of claims 1 to 7.